REMARKS/ARGUMENTS

Claim 9 has been amended to be placed in independent format, as the Office Action indicates that claim 9 includes allowable subject matter, and thus claim 9 and the claims depending therefrom are patentable. Similarly, claims 11 and 20 have been amended to include the subject matter of dependent claims 20 and 29, respectively, indicated as including allowable subject matter. Thus it is respectfully submitted that these claims and the claims depending therefrom are patentable.
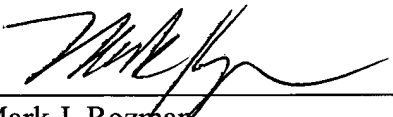
Claims 1 stands rejected under 35 U.S.C. § 103(a) over U.S. Patent No. 6,754,732 (Dixon) in view of Deitel. Applicant respectfully traverses the rejection. As to claim 1, Dixon nowhere teaches successively transferring data from a plurality of linked buffers in a first processor system to a plurality of linked buffers in a second processor system. In this regard, Dixon nowhere teaches a plurality of linked buffers from/to which data is transferred. Instead, Dixon only teaches a single transmit circular buffer and a single receive circular buffer. In this regard, the Office Action contends that the single circular buffer included in DSPs A and B of Dixon are somehow multiple linked buffers. The plain description of Dixon belies this contention. Instead, the portion of Dixon identified by the Office Action merely states that each DSP includes a single buffer. Dixon, col. 10, lns. 20-22. Simply put, there are no multiple linked buffers in Dixon. Nor does Deitel add anything in this regard. Instead, Deitel merely teaches that a linked list includes nodes each having a pointer to a next node. Still further, neither reference anywhere teaches nor suggests successive transfer via a first-in first-out buffer in the first processor system to a second first-in first-out buffer in the second processor system. Accordingly, for at least these reasons, claim 1 and the claims depending therefrom are patentable.

Pending claim 2 also stands rejected under §103(a) over Dixon in view of Deitel. Applicant respectfully traverses the rejection, at least for the same reasons discussed above regarding claim 1. Claim 2 is further patentable as the proposed combination nowhere teaches or suggests successive transfer of data between buffers arranged in a linked list. In this regard, Dixon (as discussed above) nowhere teaches or suggests such successive transfer between buffers arranged as a linked list. Neither does Deitel.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

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